

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of )

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**No. 8046.037US0**

KAZUTAKA SHIBATA and TSUNEMORI )  
YAMAGUCHI )

For: METHOD OF SURFACE- )  
MOUNTING ELECTRONIC )  
COMPONENTS )

San Francisco, California

**Box Patent Application**

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Washington, D.C. 20231

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**Dated: December 2, 1998**

**PATENT APPLICATION TRANSMITTAL**

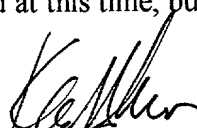
Sir:

Transmitted herewith for filing is the patent application of inventors KAZUTAKA SHIBATA and TSUNEMORI YAMAGUCHI, for "METHOD OF SURFACE-MOUNTING ELECTRONIC COMPONENTS." Enclosed are:

1. Thirteen (13) pages of the specification, including nine (9) claims and an abstract.
2. Six (6) sheets of informal drawings.

The filing fee is deferred at this time, but will be calculated and sent later.

Dated: December 2, 1998.

  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
APPLICATION FOR PATENT

METHOD OF SURFACE-MOUNTING ELECTRONIC COMPONENTS

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Background of the Invention

5           This invention relates to the technology of efficiently surface-mounting electronic components such as various kinds of semiconductor devices and elements onto a target surface, for example, of a printed circuit board.

          As is well known, the so-called solder reflow method is usually used for surface-mounting a plurality of semiconductor devices and elements to a printed  
10   circuit board. According to this method, after a solder paste is applied to the surface of terminal-forming areas prepared on the printed circuit board and the semiconductor devices and elements are placed thereon, the circuit board is placed inside a heating oven to cause the solder paste to reflow. This method is convenient because the soldering of a plurality of semiconductor devices can be carried out at  
15   once together by heating and melting the solder paste applied at various positions on the printed circuit board. This prior art method, however, has several disadvantages.

          Firstly, the solder paste must be applied to the surface of the terminal-forming areas with a high degree of accuracy by using a printing machine. If the solder paste spills over too much from the target surface, there may result an  
20   incomplete connection of conductors. In summary, the application of solder paste is a very troublesome work, and this problem is particularly serious where solder paste must be applied at a large number of places. Secondly, this method requires that the printed circuit board be kept inside the heating oven until its temperature reaches the

melting temperature of the solder. Thus, electronic components with poor heat resistance cannot be preliminarily mounted to the circuit board. In other words, the order of mounting various components onto the circuit board must be carefully planned such that components with poor heat resistance will not be subjected to a long heating process inside a heating oven.

### Summary of the Invention

It is therefore an object of this invention in view of the above to provide a more convenient and efficient method of surface-mounting electronic components to a target surface of a printed circuit board.

10 A method embodying the invention, by which the above and other objects can be accomplished, may be characterized as comprising the steps of forming an anisotropic conductive layer on the target surface, placing the electronic components on this layer such that their conductive connecting members are individually above the terminal parts in specified terminal-forming areas of the surface, and pressing these electronic components to the anisotropic conductive layer such that corresponding pairs of these conductive connecting members and the terminal parts become mutually attached and electrically conductive through this anisotropic conductive layer while the remaining portions of the layer remain electrically insulative. Such an anisotropic conductive layer may be formed either by attaching a single sheet of an anisotropic conductive film of a thermosetting synthetic resin having conductive particles dispersed therein or by applying an anisotropic conductive adhesive similarly having conductive particles dispersed inside a thermosetting resin material. This layer is preferably heated as the electronic components are pressed against it so as to become advantageously hardened.

25 The method of this invention is advantageous because the surface-mounting of electronic components to a target surface can be carried out securely and dependably. The anisotropic conductive layer can be melted and softened by

heating as a plurality of electronic components are pressed against it. As the heating temperature is increased, the softened anisotropic conductive layer can be hardened more quickly by the thermosetting characteristic of the material than if it were left to naturally become hardened. This invention therefore relates also to surface-mounted structures produced by a method of this invention as described above.

#### Brief Description of the Drawings:

The accompanying drawings, which are incorporated in and form a part of this specification, illustrate embodiments of the invention and, together with the description, serve to explain the principles of the invention. In the drawings:

Fig. 1 is a plan view of a printed circuit board to which a method of this invention may be applied;

Figs. 2A, 2B and 2C are cross-sectional and diagonal views of electronic components which may be surface-mounted by a method of this invention;

Fig. 3 is a plan view of the printed circuit board of Fig. 1 during a surface-mounting process according to this invention;

Fig. 4 is a plan view of the printed circuit board of Fig. 1 on which the electronic elements of Fig. 2A, 2B and 2C are being surface-mounted;

Fig. 5 is a sectional view of a portion of the printed circuit board of Fig. 4 on which the electronic component of Fig. 2A is placed for surface-mounting;

Fig. 6 is another sectional view of the portion of the printed circuit board of Fig. 4 after the electronic component of Fig. 2A has been surface-mounted; and

Figs. 7A and 7B are plan views of anisotropic conductive sheets embodying this invention.

#### Detailed Description of the Invention

Fig. 1 shows a printed circuit board 1 to which a method of this invention is applicable, being a rectangular plate made of a synthetic resin material such as glass epoxy resin or another kind of insulating material. Numeral 10 indicates a surface of the circuit board 1 to which electronic components are intended to be surface-mounted. A wiring pattern is formed on this surface 10 with a copper foil or the like for making connections to such electronic components.

Explained more in detail, there are formed on the surface 10 of the printed circuit board 1 two terminal-forming areas 3a and 3b for surface-mounting two semiconductor devices D1 and D2 (to be described below) and a plurality of other terminal-forming areas 3c for surface-mounting a plurality of semiconductor elements D3 (also to be described below). The area 3a is characterized as having planar rectangular terminal parts 30a positioned according to a specified arrangement at the ends of a plurality of conductive lines 2a. The area 3b is similarly characterized as having planar terminal parts 30b at the ends of another plurality of conductive lines 2b positioned according to another specified arrangement. The areas 3c include planar terminal parts 30c (positioned according to still another specified arrangement) at the ends of still another plurality of conductive lines 2c.

Numeral 4 in Fig. 1 indicates another mounting area on the surface 10 of the printed circuit board 1 for mounting one or more electronic components by other than a surface-mounting method such as by inserting pins. Numerals 40 indicates holes formed at the ends of a plurality of conductive lines 41 for inserting pin-shaped terminals of electronic components thereinto.

Similar terminal-forming and/or mounting areas may be formed on the reverse surface of the printed circuit board 1. For the convenience of description, methods of mounting electronic components on such a circuit board will not be separately described because those of mounting on the reverse surface are identical to those of mounting on the surface 10.

The semiconductor devices and elements D1, D2 and D3 are shaped respectively as shown in Figs. 2A, 2B and 2C so as to be convenient for surface-mounting, each having conductive connecting members serving as terminals or electrodes for the surface-mounting. Explained more in detail, Fig. 2A shows the semiconductor device D1 of a so-called resin-packaged J-lead type, having a semiconductor chip 60 bonded on a die pad 61 and connected through wires W to a plurality of lead terminals 6a, and a sealing resin material 62 covering the wires W and the semiconductor chip 60 to form a resin package. The lead terminals 6a are bent so as to be cross-sectionally J-shaped such that their edges are positioned on the bottom surface of this sealing resin material 62. These edge parts are utilized for the surface-mounting.

Fig. 2B shows the semiconductor device D2 of a so-called ball grid array type. It is formed, like the device D1 described above, with a semiconductor chip packaged inside a resin material 62a but is different wherein a plurality of protruding terminals 6b are formed on the bottom surface of its sealing resin 62a with solder balls. The semiconductor device D2 thus structured may be soldered to a circuit board but it can also be surface-mounted by a method according to this invention.

Fig. 2C shows one of the semiconductor elements D3 produced as a chip resistor or a chip capacitor, having metallic electrodes 6c formed on both side surfaces of a semiconductor chip 63. These electrodes 6c are formed in a step-like manner, swelling outwardly from the surface of the semiconductor chip 63 such that the semiconductor elements D3 can be surface-mounted by directly attaching these electrodes 6c on the target surface.

For surface-mounting these semiconductor devices and elements D1, D2 and D3 on the surface 10 of the printed circuit board 1, a sheet of anisotropic conductive film 5 is attached first to the surface 10, as shown in Fig. 3. As shown more clearly in Fig. 5, this anisotropic conductive film 5 may be obtained by

distributing electrically conductive particles 51 such as metallic particles inside a film with a thermosetting characteristic such as a synthetic epoxy resin film. These conductive particles 51 are dispersed inside the film 5 such that the film 5 is normally not electrically conductive. When a sufficiently strong compressive force is applied to this film 5 in the direction of its thickness, however, the density of these conductive particles 51 is increased where the force is applied. As these conductive particles 51 come to contact one another, the portions of the film 5 where such a force is applied become conductive. According to the embodiment of the invention described herein, the anisotropic conductive film 5 placed over the printed circuit board 1 serves as what is herein referred to as "the anisotropic conductive layer".

As shown in Fig. 3, the anisotropic conductive film 5 is of a size and shape so as to cover a fairly large area of the surface 10 of the printed circuit board 1 such that all of the plurality of terminal-forming areas 3a, 3b and 3c are covered together but care is taken so as to keep the area 4 uncovered thereby and exposed. The anisotropic conductive film 5 is "attached" to the surface 10 of the printed circuit board 1, but this does not mean that an affirmative effort is made to paste it to the surface 10. The act of simply placing the film 5 over the surface 10 is intended to be included although it is preferable to make some effort to make the film 5 to stick to the surface 10 such that no displacement will take place inadvertently.

Thereafter, the semiconductor devices and elements D1, D2 and D3 are placed on the anisotropic conductive film 5 as shown in Figs. 4 and 5. The device D1 is positioned such that its lead terminals 6a will be correspondingly above the terminal parts 30a in the terminal-forming area 3a. The device D2 is positioned such that its lead terminals 6b will be correspondingly above the terminal parts 30b in the terminal-forming area 3b. Similarly, the elements D3 are positioned such that their lead terminals 6c will be correspondingly above the terminal parts 30c in the terminal-forming area 3c. These devices and elements D1, D2 and D3 are pressed downward while the film 5 is heated. In Fig. 6, numeral 7 indicates a collet 7 which

may be used to place the device D1 on the film 5. In such a case, the collet 7 may be used conveniently for pressing the device D1 against the film 5. For heating the film 5, a heater may be placed beneath the printed circuit board 1. Alternatively, a heater may be incorporated in whatever component serving to press the device D1 against the film 5. As the device D1 is being pressed against the film 5, ultrasonic waves generated in a horn 70 attached to the collet 7 may be additionally made use of.

As a result of such heating, the film 5 becomes softer and portions thereof between the lead terminals 6a and the corresponding terminal parts 30a are subjected to a compressive force such that the conductive particles 51 in these portions come to contact one another between the surfaces of the lead terminals 6a and those of the terminal parts 30a, causing these lead terminals 6a to become in electrical conductive relationship with corresponding ones of the terminal parts 30a. By applying ultrasonic waves to such portions, the contacts between the lead terminals 6a and the terminal parts 30a can be made even more secure and intimate. Since the other portions of the film 5 are not subjected to any compressive force and the conductive particles 51 remain dispersed, they remain electrically non-conductive. In summary, as far as the device D1 is concerned, only its lead terminals 6a become conductive with the terminal parts 30a. The other portions of the lower surface of the device D1 contact the surface 10 of the printed circuit board 1 through the softened film 5, or more accurately through its softened resin material. The resin material of the film 5 can be hardened by increasing the heat applied to it. Thus, the device D1 can be securely attached to the printed circuit board 1 by this method. It now goes without saying that similar effects can be obtained also with the other device D2 and the elements D3.

In summary, the surface-mounting work according to this invention is very simple, including only the steps of attaching a sheet of anisotropic conductive sheet at specified positions on the surface of a printed circuit board and then mounting electronic components thereon. As a result, the cost of the final product



obtained by such a simple process becomes lower. Moreover, since the portions of the film where nothing is mounted serve as an insulating layer after the anisotropic conductive film is subjected to a heating process, the product thus obtained is better insulated.

5                   The invention has been described above with reference to only one example, but this example is not intended to limit the scope of the invention. Many modifications and variations are possible within the scope of this invention. Figs. 7A and 7B show other anisotropic conductive films 5 shaped differently. Fig. 7A shows an example characterized as having a removed edge portion 52 and Fig. 7B shows  
10 another example characterized as having an opening. With the anisotropic conductive sheet 5 thus shaped variously, positions of areas on a printed circuit board to be covered by the film can be freely adjusted. It should be reminded, however, that the present invention is not limited to the use of only one sheet of anisotropic conduct sheet. If the surface-mounting area of an electronic device is  
15 very large or its shape is very complicated, the anisotropic conductive film to be used according to this invention may be divided into a plurality of sheets. The present invention is particularly advantageous, however, when a sheet of anisotropic conduct sheet is set so as to span and cover a plurality of terminal-forming areas.

                  Neither is the present invention limited to the use of an isotropic  
20 conductive film sheet in order to form an anisotropic conductive layer. Instead of an anisotropic conductive film, use may equally well be made of an anisotropic conductive adhesive agent. Such an anisotropic conductive adhesive agent may be one obtainable by dispersing conductive particles inside a synthetic resin adhesive agent such as thermosetting epoxy resin. An anisotropic conductive layer according  
25 to this invention may be formed by applying such an adhesive agent on the surface of a printed circuit board. Such an anisotropic conductive adhesive agent need not be heated because it already has the property of causing the electronic device to become attached to the surface of the circuit board but the mounted condition of the

electronic device can be more quickly stabilized if this anisotropic conductive adhesive agent is heated than if it is allowed to harden naturally.

It also goes without saying that the present invention is not limited by the type of electronic components which are mounted. As long as the electronic components are of a kind provided with connecting conductors for use at the time of mounting such as terminals or electrodes.

What is claimed is:

1. A method of surface-mounting a plurality of electronic components having conductive connecting members to a plurality of specified terminal-forming areas of a target surface having terminal parts therein, said method comprising the steps of:
  - forming an anisotropic conductive layer on said target surface so as to span said plurality of terminal-forming areas;
  - placing said plurality of electronic components on said anisotropic conductive layer individually above said plurality of terminal-forming areas; and
  - pressing said plurality of electronic components to said anisotropic conductive layer so as to thereby cause said conductive connecting members of said plurality of electronic components to individually become adhered to and in electrically conductive relationship with a corresponding one of said terminal parts through said anisotropic conductive layer.
2. The method of claim 1 wherein said anisotropic conductive layer is formed by attaching a single sheet of anisotropic conductive film to said target surface, said anisotropic conductive film containing conductive particles dispersed inside a thermosetting synthetic resin.
3. The method of claim 1 wherein said anisotropic conductive layer is formed by applying an anisotropic conductive adhesive to said target surface, said anisotropic conductive adhesive containing conductive particles dispersed inside a thermosetting synthetic resin.

4. The method of claim 2 wherein said anisotropic conductive layer is heated as said plurality of electronic components are pressed to said anisotropic conductive layer.

5. The method of claim 3 wherein said anisotropic conductive layer is heated as said plurality of electronic components are pressed to said anisotropic conductive layer.

6. The method of claim 2 further comprising the step of preparing said single sheet of anisotropic conductive film in a shape according to positions and shapes of said terminal-forming areas of said target surface.

7. The method of claim 4 further comprising the step of preparing said single sheet of anisotropic conductive film in a shape according to positions and shapes of said terminal-forming areas of said target surface.

8. A surface-mounted structure comprising:

a circuit board with a surface having thereon a plurality of terminal-forming areas each containing terminal parts therein;

5 a plurality of electronic components having conductive connecting members thereon; and

10 an anisotropic conductive layer between and attached to both said surface of said circuit board and said plurality of electronic components, said anisotropic conductive layer being thermally hardened, electrically conductive individually between said terminal parts and corresponding ones of said conductive connecting members of said plurality of electronic components, and being electrically insulative elsewhere.

9. The surface-mounted structure of claim 8 wherein said anisotropic conductive layer comprises conductive particles dispersed inside a thermosetting synthetic resin.

Patent Application No. 2014/000000

Abstract of the Disclosure

A plurality of electronic components having conductive connecting members are surface-mounted to a target surface of a circuit board in a plurality of specified terminal-forming areas each having terminal parts. An anisotropic conductive layer is formed on this target surface so as to span these terminal-forming areas, and the plurality of electronic components are placed on this anisotropic conductive layer individually above the plurality of terminal-forming areas. As the layer is heated, these electronic components are pressed against the layer such that the conductive connecting members of the electronic components become attached to and electrically conductive with corresponding ones of the terminal parts on the circuit board. The anisotropic conductive layer remains electrically insulative elsewhere.

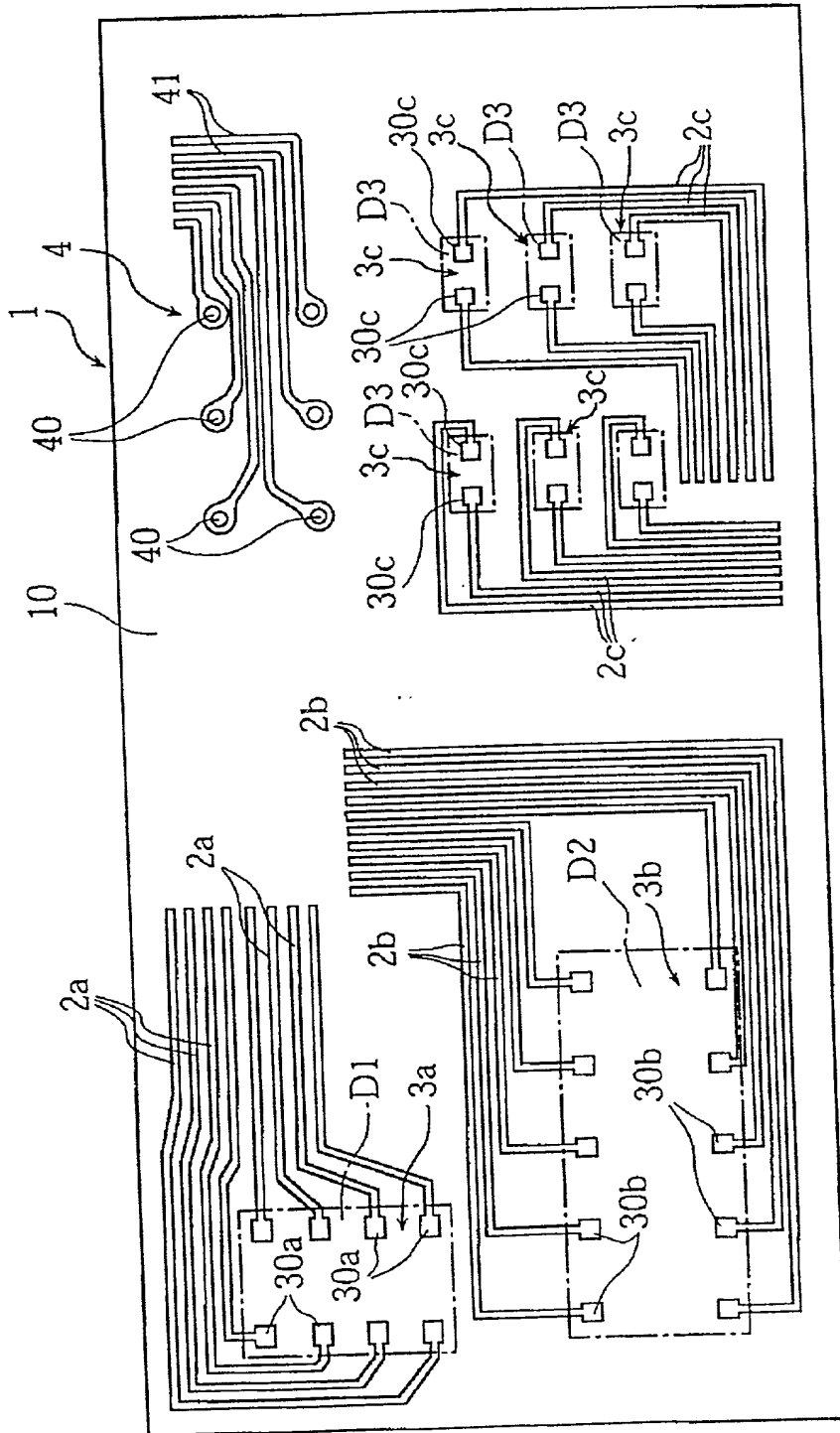


Fig. 1

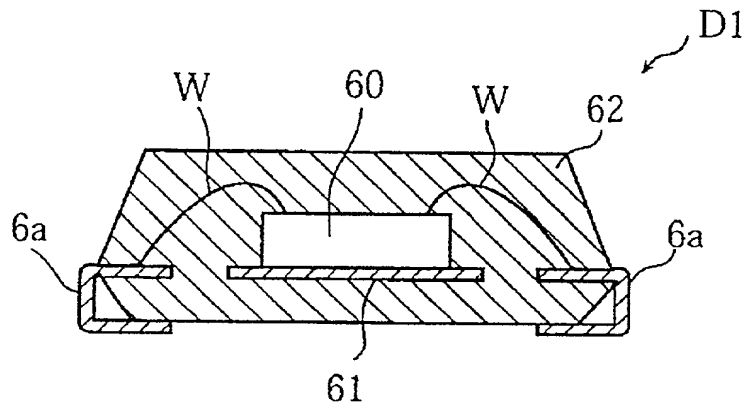


Fig. 2A

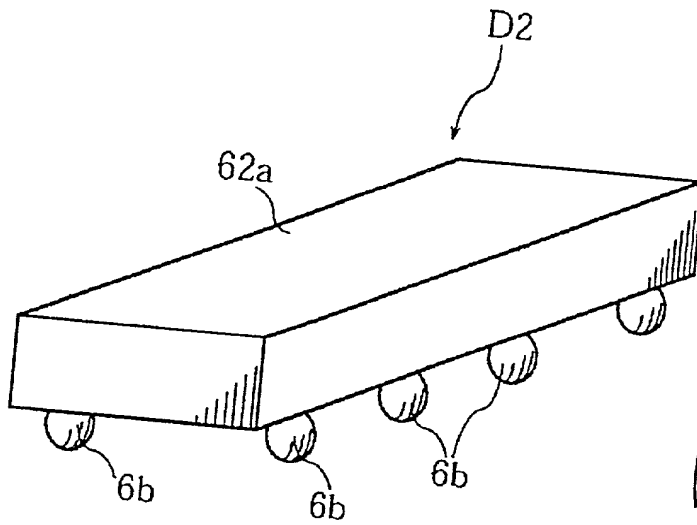


Fig. 2B

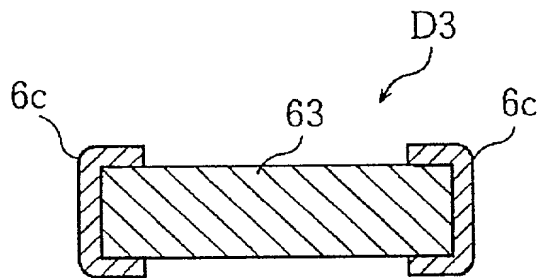


Fig. 2C



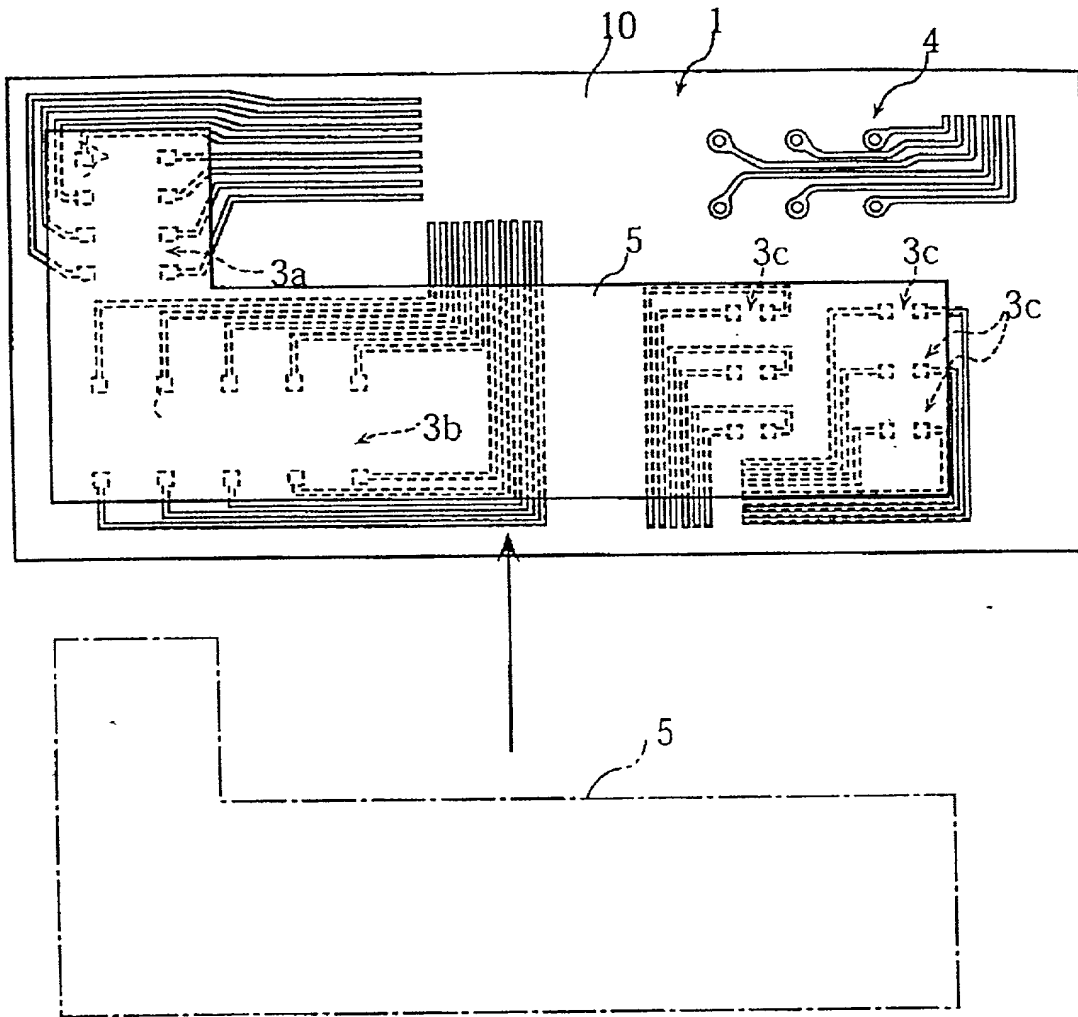


Fig. 3

BoH6-037V50

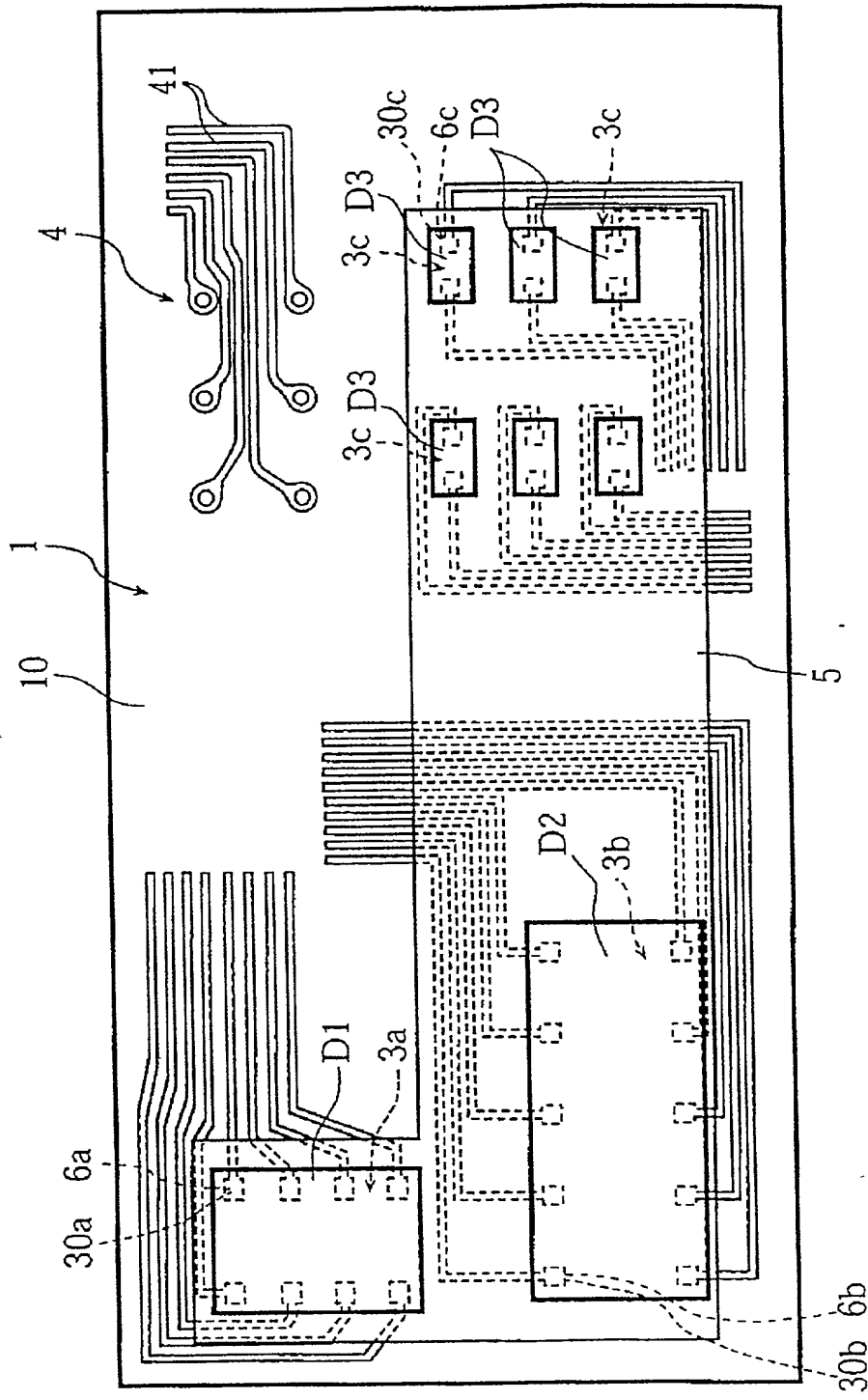


Fig. 4

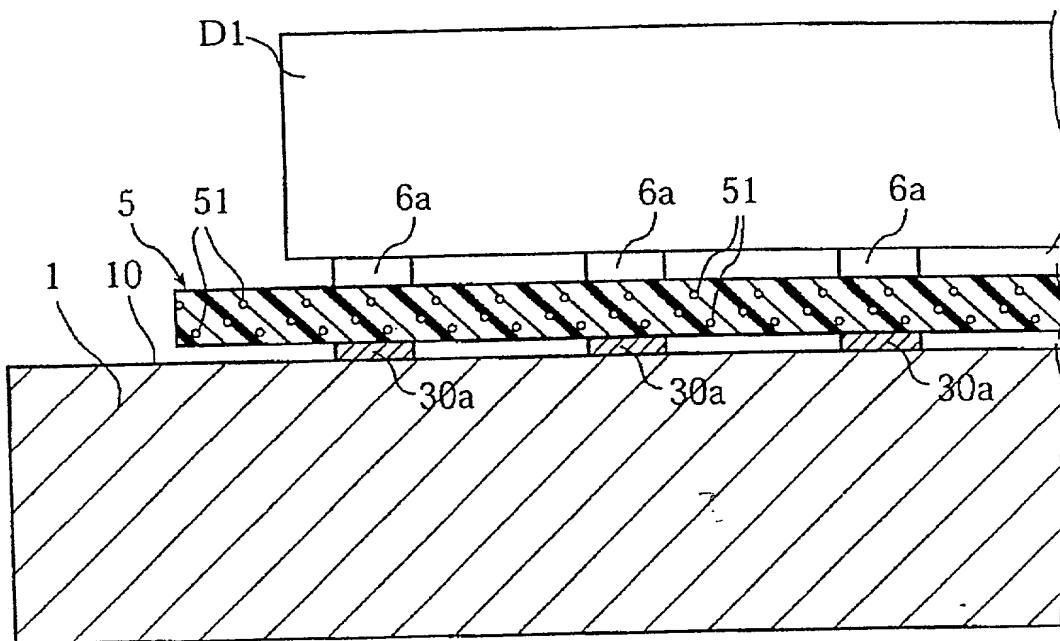


Fig. 5

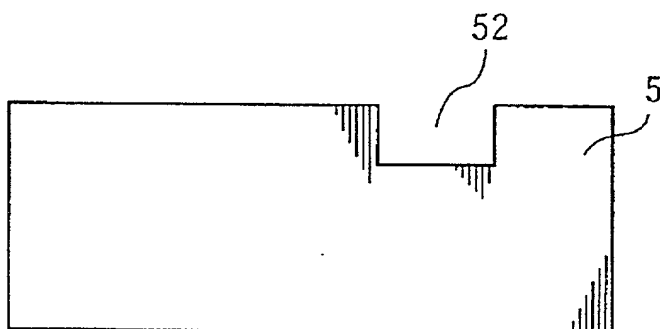


Fig. 7A

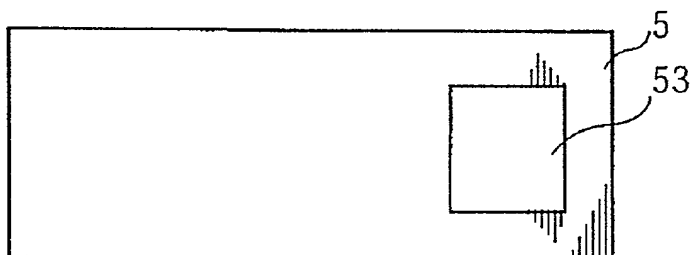


Fig. 7B

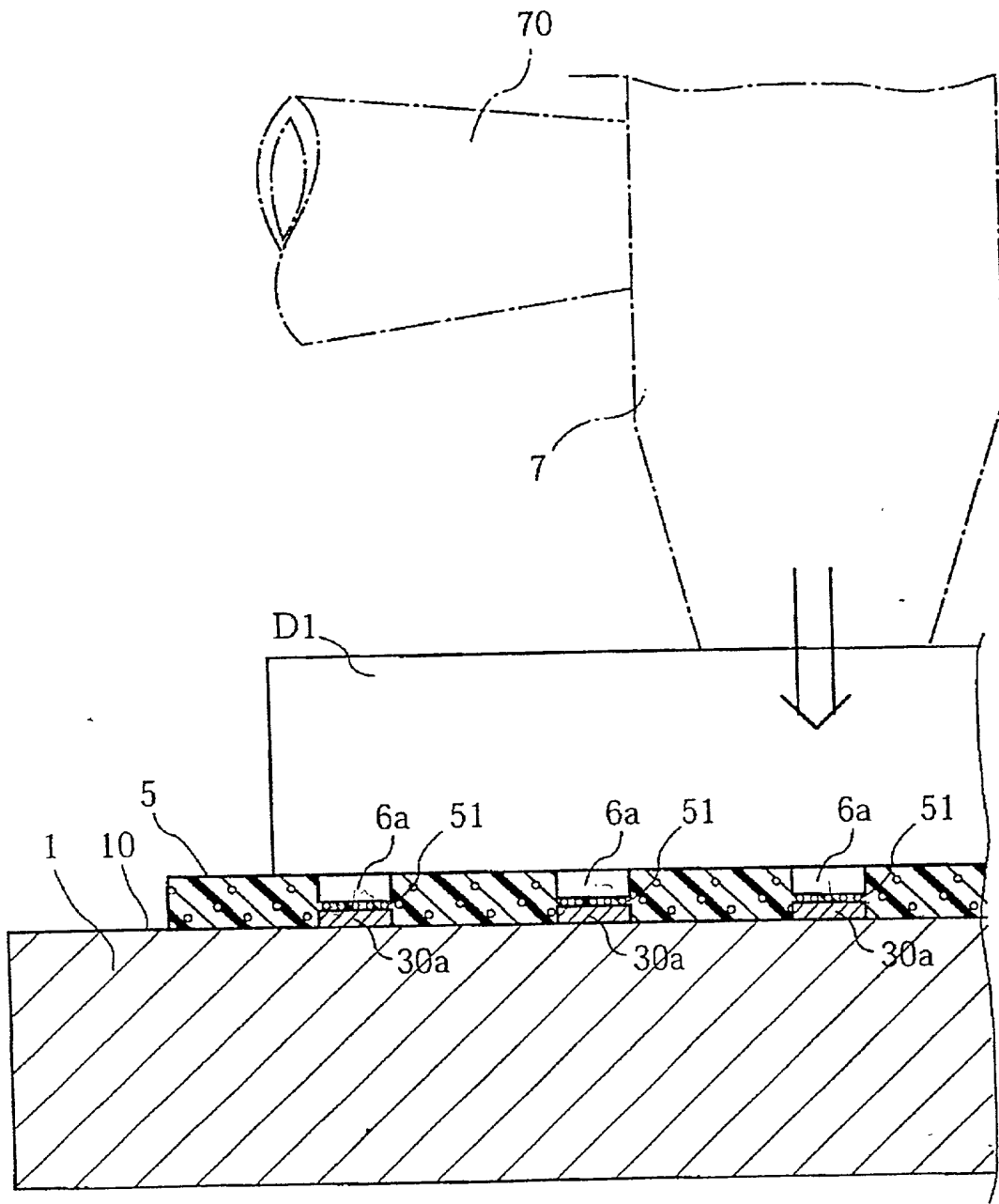
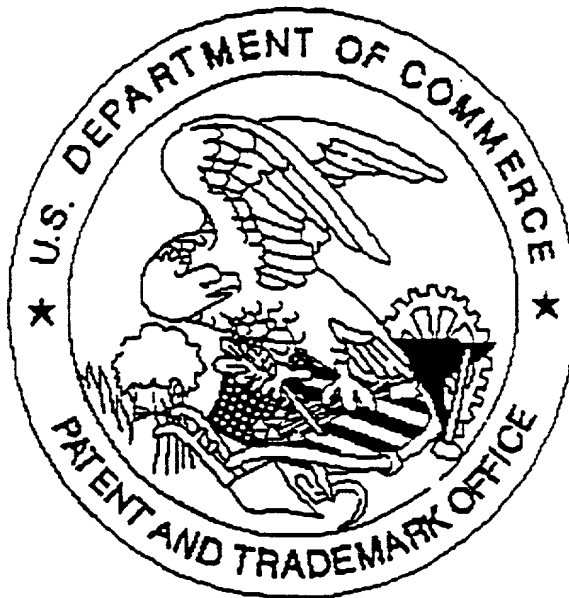


Fig. 6

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